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10/595,232	03/28/2006	Yasunori Ito	36856.1417	5208
MURATA MANUFACTURING COMPANY, LTD. C/O KEATING & BENNETT, LLP 1800 Alexander Bell Drive SUITE 200 Reston, VA 20191			EXAMINER	
			SAN MARTIN, JAYDI A	
			ART UNIT	PAPER NUMBER
			2837	
		•	NOTIFICATION DATE	DELIVERY MODE
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			09/09/2009	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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Application/Control Number: 10/595,232

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DETAILED ACTION

Allowable Subject Matter

1. Prosecution on the merits of this application is reopened on claims 10-19 considered unpatentable for the reasons indicated below.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 10, 12-13, 16, 19 and 20 are rejected under 35 U.S.C. 102(b) as being anticipated by Asakura (JP 11-111554).
- 4. Regarding claim 10, in figure 2, Asakura discloses a lamination-type resistance element (41) comprising:
- a laminated sinter having a plurality of ceramic resistance layers (22) and a plurality of internal electrodes (28-31, 23-24, 42-45) laminated therein; and
- a first external electrode (26) and a second external electrode (27) provided on the outer surface of the laminated sinter; wherein

the plurality of internal electrodes includes a plurality of internal electrodes of a first group (28-31) and a plurality of internal electrodes of a second group (42-45);

each of the plurality of internal electrodes of the first group includes a resistance unit in which at least two internal electrodes are disposed so as to face each other through one of the ceramic resistance layers (electrode 28 faces electrode 30 through the ceramic layer formed

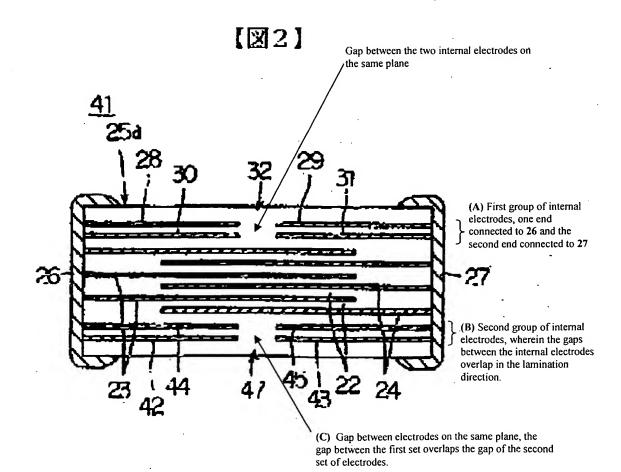
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between them), a first end of the resistance unit is electrically connected to the first external electrode (26), and a second end is electrically connected to the second external electrode (27);

each of the internal electrodes of the second group (A, please refer to the figure below) includes a plurality of pairs of internal electrodes (42-45) in which a first end of one electrode faces a first end of the other electrode with a gap therebetween (C) on the same plane inside the laminated sinter, one internal electrode in each pair is electrically connected to the first external electrode, and the other is electrically connected to the second external electrode (42 and 44 are connected to 26; 43 and 45 are connected to 27) and

the gaps between the first ends of each of the plurality of pairs of internal electrodes of the second group overlap with each other in a lamination direction of the laminated sinter (C, refer to the figure below).



- 5. Regarding claim 12, note that internal electrode 28 faces 29 with a gap on the same plane and electrode 30 faces electrode 31 in the same manner.
- 6. Regarding claim 13, note that the gap of the first group overlap the gap of the second group.
- 7. Regarding claim 16, the limitation is anticipated as explained above and shown in the figure.
- 8. Regarding claim 19, Asakura discloses a lamination-type resistance element (41, as shown above in figure 2)comprising:

a laminated sinter having a plurality of ceramic resistance layers (22) and a plurality of internal electrodes (28-31, 23-24, 42-45)laminated therein; and

a first external electrode (26) and a second external electrode (27) provided on the outer surface of the laminated sinter; wherein

the internal electrodes include internal electrodes of a first group (A) and internal electrodes of a second group (B);

each of the internal electrodes of the first group includes a first internal electrode connected to the first external electrode (28 and 30 connected to 26) (and a second internal electrode connected to the second external electrode 29 and 31 connected to 27) which face each other through the ceramic resistance layer (28 faces 30 and 29 faces 31)

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each of the internal electrodes of the second group (B) includes a third internal (44) electrode and a fourth internal electrode (45) in which a first end of third internal electrode faces a first end of the fourth internal electrode with a gap therebetween on the same plane inside the laminated sinter (C – refer to the figure above), and second ends are connected to the first external electrode (26) and the second external electrode (27), respectively, and the gaps between the third internal electrodes and fourth internal electrodes are at the same location along the lamination direction of the laminated sinter (gap C overlaps in the lamination direction); and

an end of the internal electrode of the first group that is arranged closest to the second group overlaps (end of electrode 30), in the lamination direction of the laminated sinter, with the first end of one of the third (end of electrode 44) and fourth internal electrodes that is arranged closest to the first group.

9. Regarding claim 20, the gaps between the first ends of each of the plurality of pairs of the internal electrodes of the second group overlap with each other in the lamination direction as required.

Allowable Subject Matter

- 10. Claims 17 and 18 allowed.
- 11. Claims 14 and 15 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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12. The references of the Prior Art of Record fail to teach or suggest either alone or in obvious combination the limitations as set forth in claims 17 and 18, and specifically comprising the limitation of the gaps being formed at different locations when seen from the lamination direction of the laminated sinter (claims 14 and 17), and the no-connection-type internal electrode which is arranged to lie on top of the first internal electrode and the second internal electrode through the ceramic resistance layer in the lamination direction of the sinter and which is not connected to the external electrodes (claims 15 and 18).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to J. SanMartin whose telephone number is (571)272-2018. The examiner can normally be reached on M-Th 9-7.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Walter Benson can be reached on 571-272-2227. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Jaydi SanMartin/ Primary Examiner, Art Unit 2837

8/26/2009